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 September 2008 [Communications of the ACM](#)
 Publisher: ACM

Full text available: [Digital Edition](#), [Pdf \(8.68 MB\)](#) [Additional Information: full citation](#)

Bibliometrics: Downloads (6 Weeks): 876, Downloads (12 Months): 2289, Cita

- 2 [Flush: a reliable bulk transport protocol for multihop wireless network](#)
 Sukun Kim, Rodrigo Fonseca, Prabal Datta, Arsalan Tavakoli, David Culler,
 Stoica

November 2007 [SenSys '07: Proceedings of the 5th international conference sensor systems](#)

Publisher: ACM

Full text available: [Pdf \(421.18 KB\)](#) [Additional Information: full citation, abstract, i](#)

Bibliometrics: Downloads (6 Weeks): 42, Downloads (12 Months): 446, Citatio

We present Flush, a reliable, high goodput bulk data transport protocol
 Flush provides end-to-end reliability, reduces transfer time, and adapts
 conditions. It achieves these properties using end-to-end ...

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Keywords: interference, transport, wireless sensor networks

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 YongChul Kwon, Magdalena Balazinska, Albert Greenberg
 August 2008 [Proceedings of the VLDB Endowment](#), Volume 1 Issue 1
 Publisher: VLDB Endowment

Full text available: [Pdf \(711.54 KB\)](#) [Additional Information: full citation, abstract, i](#)

Bibliometrics: Downloads (6 Weeks): 36, Downloads (12 Months): 37, Citation

We present SGuard, a new fault-tolerance technique for distributed stre
 running in clusters of commodity servers. SGuard is less disruptive to n
 leaves more resources available for normal stream ...

- 4 [The Reconfigurable Streaming Vector Processor \(RSVPTM\)](#)
 Silviu Ciricescu, Ray Essick, Brian Lucas, Phil May, Kent Moat, Jim Norris, B
 December 2003 [MICRO 36: Proceedings of the 36th annual IEEE/ACM Inter](#)
 Microarchitecture
 Publisher: IEEE Computer Society

Full text available:  Pdf (305.63 KB)

Additional Information: full citation, abstract, i

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 60, Citation (

The need to process multimedia data places large computational demand on devices. These multimedia functions share common characteristics: they are data-streaming, performing the same operation(s) on many data ...

5 EXOCHI: architecture and programming environment for a heterogeneous multithreaded system



Perry H. Wang, Jamison D. Collins, Gautham N. Chinya, Hong Jiang, Xinming Yang, Guai-Yuan Lueh, Hong Wang

June 2007 PLDI '07: Proceedings of the 2007 ACM SIGPLAN conference on programming language design and implementation

Publisher: ACM

Full text available:  Pdf (242.12 KB)

Additional Information: full citation, abstract, i

Bibliometrics: Downloads (6 Weeks): 101, Downloads (12 Months): 742, Citation (

Future mainstream microprocessors will likely integrate specialized access to single die to achieve better performance and power efficiency. However, programming such a heterogeneous multicore platform, ...

Keywords: GPU, heterogeneous multi-cores, openMP

Also published in:

June 2007 SIGPLAN Notices Volume 42 Issue 6


6 Visual attention based information culling for Distributed Virtual Environments



Ashweeni Kumar Beaharee, Adrian J. West, Roger Hubbard

October 2003 VRST '03: Proceedings of the ACM symposium on Virtual reality systems and technology

Publisher: ACM

Full text available:  Pdf (253.03 KB)

Additional Information: full citation, abstract, i

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 59, Citation (



The central goal in Distributed Virtual Environments (DVEs) is to provide a virtual world that is perceptually plausible for the immersed user. This has to do with network issues such as bandwidth limitations and ...

7 Communications of the ACM: Volume 51 Issue 7



July 2008 Communications of the ACM

Publisher: ACM

Full text available:  Digital Edition,  Pdf (6.54 MB) Additional Information: full citation, abstract, i

Bibliometrics: Downloads (6 Weeks): 4787, Downloads (12 Months): 6221, Citation (


8 A simple mechanism for capturing and replaying wireless channels



Glenn Judd, Peter Steenkiste

August 2005 E-WIND '05: Proceedings of the 2005 ACM SIGCOMM workshop on wireless network design and analysis

Publisher: ACM

Full text available:  Pdf (6.06 MB)

Additional Information: full citation, abstract, i

Bibliometrics: Downloads (6 Weeks): 80, Downloads (12 Months): 502, Citation

Physical layer wireless network emulation has the potential to be a powerful and important challenge in physical emulation, and traditional simulation, is a challenge. In this paper we examine the possibility ...

Keywords: channel capture, emulation, wireless

9 Reducing Design Complexity of the Load/Store Queue

Il Park, Cheng Liang Qiu, T. N. Vijaykumar

December 2003 MI CRO 36: Proceedings of the 36th annual IEEE/ACM International Symposium on Microarchitecture

Publisher: IEEE Computer Society

Full text available:  Pdf (174.73 KB) Additional Information: full citation, abstract, ...

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 33, Citation

With faster CPU clocks and wider pipelines, all relevant microarchitecture designs must be scaled accordingly. There have been many proposals for scaling the issue queue and branch prediction hierarchy. However, nothing has been done for scaling the ...

10 Conjoined-Core Chip Multiprocessing

Rakesh Kumar, Norman P. Jouppi, Dean M. Tullisen

December 2004 MI CRO 37: Proceedings of the 37th annual IEEE/ACM International Symposium on Microarchitecture

Publisher: IEEE Computer Society

Full text available:  Pdf (369.99 KB) Additional Information: full citation, abstract, ...

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 74, Citation

Chip Multiprocessors (CMP) and Simultaneous Multi-threading (SMT) are proposed to increase processor efficiency. We believe these two approaches represent a viable spectrum. Between these two extremes, there exists ...

11 Balanced Multithreading: Increasing Throughput via a Low Cost Multi-Threaded Processor

Eric Tune, Rakesh Kumar, Dean M. Tullisen, Brad Calder

December 2004 MI CRO 37: Proceedings of the 37th annual IEEE/ACM International Symposium on Microarchitecture


Publisher: IEEE Computer Society

Full text available:  Pdf (197.41 KB) Additional Information: full citation, abstract, ...

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 50, Citation

A simultaneous multithreading (SMT) processor can issue instructions from multiple threads, allowing it to effectively hide various instruction latencies; this effect increases the processor's utilization. However, each ...

12 GPGPU: general purpose computation on graphics hardware

 David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Leifohn

August 2004 SIGGRAPH '04: SIGGRAPH 2004 Course Notes

Publisher: ACM


Full text available:  Pdf (63.03 MB) Additional Information: full citation, abstract, ...


Bibliometrics: Downloads (6 Weeks): 283, Downloads (12 Months): 1460, Cita

The graphics processor (GPU) on today's commodity video cards has ev
and flexible processor. The latest graphics architectures provide tremen
computational horsepower, with fully programmable vertex ...

13 Queue: Volume 6 Issue 4

July 2008 Queue

 Publisher: ACM

Full text available:  Digital Edition ,  Pdf (6.13 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 142, Downloads (12 Months): 272, Citati

Flash is growing from mere curiosity to a new tier in the storage hierarc

14 Interactions: Volume 15 Issue 3


May 2008 Interactions

 Publisher: ACM

Full text available:  Digital Edition ,  Pdf (7.95 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 198, Downloads (12 Months): 791, Citati

15 Evaluation of the field-programmable cache: performance and energ

 Domingo Benitez, Juan C. Moura, Dolores I. Rexachs, Emilio Luque

May 2006 CF '06: Proceedings of the 3rd conference on Computing fronti

Publisher: ACM


Full text available:  Pdf (299.74 KB) Additional Information: [full citation](#), [abstract](#), [info](#)

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 31, Citation (

Many authors have proposed power management techniques for genera
of degraded performance such as lower IPC or longer delay. Some prop
memories because they consume a significant fraction of ...

Keywords: adaptive processors, performance evaluation, reconfigurabl
adaptation, static and dynamic energy consumption

16 Continuous profiling: where have all the cycles gone?

 Jennifer M. Anderson, Lance M. Berg, Jeffrey Dean, Sanjay Ghemawat, Mor
Leung, Richard L. Sites, Mark T. Vandevoorde, Carl A. Waldspurger, William

November 1997 Transactions on Computer Systems (TOCS) , Volume 15 I

Publisher: ACM

Full text available:  Pdf (259.35 KB) Additional Information: [full citation](#), [abstract](#), [info](#)


Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 107, Citatio

This article describes the Digital Continuous Profiling Infrastructure, a s
designed to run continuously on production systems. The system suppo
unmodified executables, and collects profiles ...

Keywords: performance understanding, performance-monitoring hardw

[Transparent control independence \(TCI\)](#)

Ahmed S. Al-Zawawi, Vimal K. Reddy, Eric Rotenberg, Haltham H. Akkary
June 2007 ISCA '07: Proceedings of the 34th annual international symposium
Publisher: ACM

Full text available:  Pdf (391.64 KB) Additional Information: [full citation](#), [abstract](#), [bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 90, Citation

Superscalar architectures have been proposed that exploit control independence performance penalty of branch mispredictions by preserving the work of independent instructions. The essential goal of exploiting control ...

Keywords: branch prediction, checkpoints, control independence, selection recovery, speculation

Also published in:

June 2007 SIGARCH Computer Architecture News Volume 35 Issue 2

18 [ML primitives for querying a fragmented world](#)

Peter A. Boncz, Martin L. Kersten
October 1999 The VLDB Journal — The International Journal on Very Large Databases
2

Publisher: Springer-Verlag New York, Inc.


Full text available:  Pdf (261.36 KB) Additional Information: [full citation](#), [abstract](#), [bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 49, Citation (6 Weeks): 1

In query-intensive database application areas, like decision support and data warehousing, vertical fragmentation have a significant performance advantage. In order to support oriented applications on top of such a fragmented ...

Keywords: Database systems, Main-memory techniques, Query languages, Vertical fragmentation

19 [Exploiting access semantics and program behavior to reduce snooping](#)

 Chinnakrishnan S. Ballapuram, Ahmad Sharif, Hsien-Hsin S. Lee
March 2008 ASPLOS XIII: Proceedings of the 13th international conference on programming languages and operating systems

Publisher: ACM


Full text available:  Pdf (226.27 KB) Additional Information: [full citation](#), [abstract](#), [bibliometrics](#)

Bibliometrics: Downloads (6 Weeks): 46, Downloads (12 Months): 334, Citation

Integrating more processor cores on-die has become the unanimous trend in the industry. Most of the current research thrusts using chip multiprocessors to analyze problems in various domains. One of the main design ...

Keywords: MESI protocol, chip multiprocessors, internal and external cache

20 [Fast triangle reordering for vertex locality and reduced overdraw](#)

 Pedro V. Sander, Diego Nebab, Joshua Barczak

August 2007 SIGGRAPH '07: SIGGRAPH 2007 papers

Publisher: ACM

Full text available:  [Mov](#) (25:35 MIN),  [Pdf](#) (2.74 MB) Additional Information: [full citation](#)

Bibliometrics: Downloads (6 Weeks): 26, Downloads (12 Months): 301, Citatio

We present novel algorithms that optimize the order in which triangles are transformed to improve vertex cache efficiency as well as for view-independent overdraw triangle orders perform on par with previous methods, ...

Also published in:

July 2007 Transactions on Graphics (TOG) Volume 26 Issue 3

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